

WHAT IS CLAIMED IS:

1. A control apparatus for a separation type terminal having a master unit and a slave unit in a W-CDMA WLL system, the control apparatus comprising:
- 5 a mirror register local to the master unit and containing a mirror register data;
a slave register local to the slave unit and containing a slave register data; and
a transmission path,
wherein data is transmitted between the mirror register and the slave register
through the transmission path so that the mirror register and the slave register data are the
10 same.
2. The apparatus of claim 1, wherein the mirror register data and the slave register data are periodically transmitted and received.
- 15 3. The apparatus of claim 1, wherein the mirror register and the slave register are the same in type and capacity.
4. The apparatus of claim 1, further comprising:
a master controller local to the master unit and containing a master framing
20 unit; and
a slave controller local to the slave unit and containing a slave framing unit,

wherein the transmission path connects the master controller to the slave controller and is a link obtained based on the amount of data processed by the slave controller.

5 5. The apparatus of claim 1, further comprising:

a master frame logic unit local to the master unit;

a slave frame logic unit local to the slave unit,

wherein the data transmission and receiving operations between the mirror register and the slave register are performed periodically through a time slot allocated by the master and slave frame logic units.

10 6. A control apparatus for a separation type terminal in a W-CDMA WLL system, comprising:

a master unit having a mirror register;

15 a slave unit having a slave register and controlled by the master unit; and

a transmission path for implementing a data transmission and receiving operation between the master unit and the slave unit.

20 7. The apparatus of claim 6, wherein the master unit comprises:

a microprocessor for controlling the W-CDMA WLL terminal;

a decoding logic unit for interpreting a control instruction of the microprocessor; and

a framing logic unit for transmitting data to and receiving data from the mirror register and the slave unit and storing the received data in the slave register.

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8. The apparatus of claim 6, wherein the slave unit comprises:

a plurality of slave registers for storing data of the slave unit and storing data transmitted from the master unit; and

10 a framing logic unit for transmitting and receiving data between the mirror register and the slave unit, and for storing in one of the plurality of slave registers next operation data, state data of the slave unit, and received data.

15 9. The apparatus of claim 7, wherein the mirror register is formed of a register having a same capacity and type as the slave register.

10. The apparatus of claim 7, wherein the framing logic unit adjusts a frame synchronization of the master unit and the slave unit when transmitting the data using a certain clock signal as a sampling clock signal.

11. The apparatus of claim 7, wherein the framing logic unit periodically transmits a data of the mirror register to a framing logic unit of the slave unit through an allocated time slot.

5 12. The apparatus of claim 7, wherein the framing logic unit periodically receives slave register data through an allocated time slot and stores it in the mirror register.

13. The apparatus of claim 7, wherein the mirror register receives slave register data at a certain period and stores the same.

14. The apparatus of claim 8, wherein the framing logic unit periodically transmits slave register data to a framing logic unit of the master unit through an allocated time slot.

15. The apparatus of claim 8, wherein the framing logic unit periodically receives mirror register data through an allocated time slot and stores it in the slave register.

16. The apparatus of claim 7, wherein the framing logic unit periodically performs a data transmission through a framing logic unit of the slave unit and the transmission path.

17. The apparatus of claim 8, wherein the framing logic unit periodically performs a data transmitting and receiving operation through a framing unit of the master unit and the transmission path.

5 18. The apparatus of claim 6, wherein the transmission path is a transmission link formed based on the amount of data processed by the slave unit and has a connection structure between the slave unit and the master unit.

10 19. The apparatus of claim 6, wherein the mirror register contains data that is the same as data contained by the slave register, and the master unit uses the mirror register to control the slave unit.

15 20. A method of controlling a slave unit in a separation type terminal in a W-CDMA WLL system, the method comprising:

transmitting data between a master unit and the slave unit such that data contained in the master unit is a duplicate of data contained in the slave unit, the slave unit being located remotely from the master unit; and

controlling the slave unit with the master unit based on data stored in the master unit and received from the slave unit.

21. The method of claim 20, wherein the data is periodically transmitted and received by the master unit and the slave unit.

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